

FEATURES

- AMBA® 2.0 Compatible
- Provides ability to remap Flash or SRAM to boot location
- Useful for software development as well as product deployment

LICENSED IP PACKAGE INCLUDES

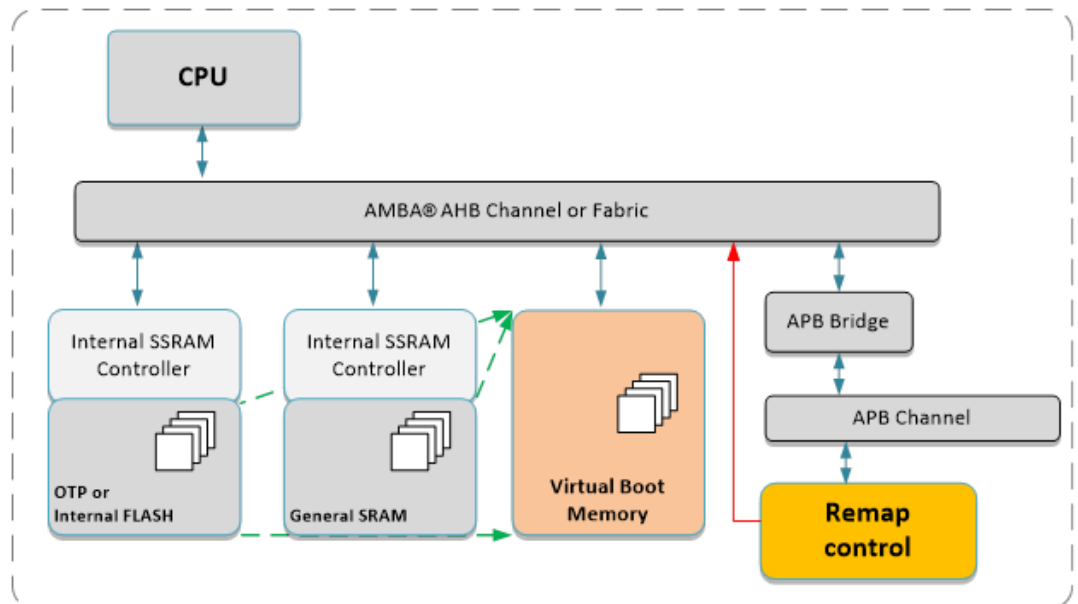
- Verilog Source
- Complete Test Environment

DESCRIPTION

The APB Remap Module is a standard APB peripheral that provides a two-bit output signal that may be used by an AHB Address Decoder module to implement remap functionality. A Remappable area is an area of decodable address space that can be configured to point to one of several locations, e.g. SRAM, FLASH, etc.

In a typical processor-based system, a processor will boot from a specific area of the memory map. Usually, non-volatile memory (e.g. FLASH) is assigned to the processor's boot area in the memory map, but there are instances when it is necessary for volatile memory (e.g. SRAM) to be located at the boot area. The Remap module register provides a two-bit signal to the address decoder. The address decoder uses the two-bit remap signal to select one of (up to) four memory areas to be placed at the processor boot location.

GENERAL USE



You may also be interested in:

AMBA® Subsystems

- Low Power Subsystem (simple AHB system)
- Low Power / Performance Subsystem (includes AHB Multi-matrix Fabric)
- Custom Performance Subsystem (includes AXI Multi-layer Fabric)

IP Cores

Infrastructure Cores

AHB Multi-Matrix Fabric
AHB/AHBLite Channel
AHB Arbiter
AXI Multi-Layer Fabric
AXI to AHBLite Bridge
AXI to APB Bridge
AHB to ABP Bridge
APB Channel

AXI Cores

AXI Multi-Layer Fabric
AXI to AHBLite Bridge
AXI to APB Bridge
AXI External Bus Interface
(Memory/Flash Controller)
AXI Internal Memory Controller
AXI QSPI with Execute in Place (XIP)

AHB Cores

AHB Channel
AHB Multi-Matrix Fabric
AHB to ABP Bridge
AHB Arbiter
AHB QSPI with Execute in Place (XIP)
AHB External Bus Interface
AHB Internal SRAM Controller
AHB Interrupt Controller
AHB DMA Controller
AHB DMA 4 Channel Controller
AHB TFT LCD Controller
AHB DES/TDES Encryption/Decryption

AHB Serial Flash Controller
Octal, Quad, Dual and Single Modes

Serial to AHB Bridge
SPI slave to AHB Master
Monitor/Control

APB Cores

APB Channel
APB Quad SPI Controller
APB General Purpose IO
APB Timer
APB UART
APB I2C (Master and Slave)
APB SPI
APB Watchdog Timer
APB Pulse Width Modulator
APB Real Time Clock

General

DES – Digital Encryption Standard
Triple DES (Low Gates)
Triple DES (pipelined)
ADC Interface (semi-custom)
Mixed-Signal Interfaces (semi-custom)
Power Management Unit (semi-custom)

AES Encryption Core

For more information contact



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